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## APPLICANTS

Toru Tanzawa, Ebina-shi, JAPAN;

\*\* CONTINUING DATA

NONE DL

\*\* FOREIGN APPLICATIONS

YES DL

JAPAN 2002-206177 07/15/2002 ✓

JAPAN 2003-193728 07/08/2003 ✓

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature <i>[Signature]</i> Initials <i>DL</i>	STATE OR COUNTRY JAPAN	SHEETS DRAWING 12	TOTAL CLAIMS 21	INDEPENDENT CLAIMS 6
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## TITLE

Nonvolatile semiconductor memory capable of generating read-mode reference current and verify-mode  
 reference current from the same reference cell

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